USN	//										HEC74
DE DE DE CONTRACTOR DE CONTRAC											
Seventh Semester B.E. Degree Examination, Dec.2017/Jan.2018											
Embedded System Design											
Time: 3 hrs. Max. Marks: 100											
Note: Answer any FIVE full questions, selecting at least TWO questions from each part.											
PART - A											
1	a.	Defi	ne a	n er	nhei	dded	system	n E	xnla	in the traditional approach of an embedded sy	stem design
•	u.	with	nea	t flo	wd	hagra	im.				(08 Marks)
	b.			e tl	ne c	liffer	ent co	ompi	uting	g engines available in the embedded system	
	c.	brie:	tly. vicro	nro	2000	or si	nport	144	inst	ruction and has 256 register. Write the instruc	(06 Marks)
	C.	usin	g 32	2-bit	inst	truct	ion reg	siste	r for	1-adder, 2-adder and 3-adder using big Endi	an and little
		End					0	(0	No.	(06 Marks)
2	a.	Exp	lain	the	va	rious	types	of	add	ressing modes used in embedded system	design with
		exar	nple						9		(06 Marks)
	b.	Wha	at ar	e the	e ba	SIC O	peration	on ca	in bo	performed on a register and also explain RT	(08 Marks)
	c.	Wha	at is	stat	e dia	agrai	n? Bri	efly	expl	ain FSM (Finite State Machine) with a block	diagram.
											(06 Marks)
3	a.						ation o				(06 Marks) (06 Marks)
	b. c.	Exp	lain	the	varı	ous : nean	cach 'cach	es av	'ana	ble in DMA on also illustrate cache dire	
	С.		methods. (08 Marks)								
4	a.	Exp	lain	wat	erfa	11 1if	e cycle	e mo	déf	along with steps.	(08 Marks)
	b.	Brie	efly	expl	ain	the v	arious	kin	ds o	f cohesion.	(06 Marks)
	c.	Dis	cuss	fun	ctio	nal v	ersus	irch	tect	ure model.	(06 Marks)
							6	T		PART - B	
5	a.	Wh	at is	ope	rati	ng sy	stem?	Exp	lain	the roles and responsibilities of each compon	ent. (08 Marks)
	b.	Exp	lain	abo	ut (i) pr	cess a	and t	hrea	ds, (ii) sharing resources.	(06 Marks)
	c.	Wh	at ai	e th	e va	riqu	kinds	ofs	stack	used in embedded application? Briefly expla	in.(06 Marks)
6	a.	Wh	at is	TC	B?	Also	illustr	ate 7	CB	and its C-code structure.	(08 Marks)
	b.	Exp	olain	abo	ut	ORI	EGRO	UNI) an	d BACKGROUND systems.	(05 Marks)
	c.	Des	scrib	e m	emó	ry m	anage	men	t rev	risited.	(07 Marks)
7	a.	Wh	at d	o vo	u m	ean 1	by 'pe	rforr	nanc	ee' or 'efficiency measure' in an embedded sy	stem? (% Marks)
	b.	A.S.	cute	an	d ex	nlaiı	the fo	allov	ving	program using looping constructs flow:	(Narks)
	0.	1	Pro	gran	n co	de: i	nt sum	$\mathbf{l} = 0$,		
	_	1	~			fc				N; i++)	(08 Marks)
	c.	Rri	efly	evn	lain	the		1 = s		tal operations in DATA STRUCTURE.	(06 Marks)
Lies		What are hardware accelerators? Explain the use of hardware accelerator with example.									
8	a.	Wh	nat a	re h	ardv	vare	accele	rato	s? E	explain the use of nardware accelerator with ea	(06 Marks)
	b.	Exp	plair	the	bas	sic el	ement	s use	ed in	analyzing flow of control.	(08 Marks)
	C	Ho	w de	2 1/0	II re	duce	nowe	r cor	ISIIm	nption in software?	(06 Marks)